

## **CURRICULUM VITAE**

### **Dr. RACHAPUDI PRABHAKAR**

H-No. 45/24-R-30-A1

Ashok Nagar

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### **Objective**

Seeking a position as professional in the organization, which would give me a chance to improve my knowledge and enhance skills in the state of art technologies and be a part of the team that excels in work towards the growth of the organization and gives me satisfaction.

### **Educational Qualifications**

- **Ph.D from JNTUA Anantapur, A P, India** in the month of Aug,2017 in the field of VLSI.
- **Master of Technology (M.Tech) in Specialization in “DSCE”** in 2005 with 71% from JNTUCEA, Anantapur, JNTU, Hyderabad in 2005.
- **Bachelor of Technology (B.Tech) in Electronics & Communications Engineering** from GPREC, Kurnool in 1995.

### **Work experience**

**Total 19 years of Experience in Teaching.**

- ❖ Working as Professor of ECE & Vice Principal in **Dr K V Subba Reddy Institute of Technology**, Kurnool, since 01.07.2015 to till date.
- ❖ Worked as Associate Professor in **Audisankara Institute of Technology**, Gudur, Nellore Dist from 30.06.2014 to 02.06.2015.
- ❖ Worked as Associate Professor in **Holy Mary Institute of Technology and Science**, Bogaram, Keesara R R Dist from 01.07.2011 to 30.05.2014.

- ❖ worked as Associate Professor in **KITE**, Edulabad R R Dist from 23.12.2009 to 30.06.2011
- ❖ Worked as Associate professor in **SKTRMCE, Kondair, Mahabub Nagar** from 16-July-2007 to 4 –Dec- 2009.
- ❖ Worked as Associate Professor in **SSJ Engineering College, Mahabub Nagar** from 03.12.2000 to 30.06.2007
- ❖ Worked as Lecturer in **SGIET**, Markapur, Prakasam(Dist) from 20.12.1999 to 01.12.2000

### **List of Publications & Conferences**

1. **International Journal of Computer Applications**, Vol.No:13, Issue No.1, (ISBN: 978-93-80746-54-4), Aug, 2011, pp.34-35.
2. **International Journal of Engineering Research and Applications**, Vol.No:2, Issue No.4 (ISSN: 2248-9622), July-Aug, 2012, pp.1181-1184.
3. **International Organization of Scientific Research Journal of VLSI & Signal Processing**, Vol.N0.1, Issue No. 3, (ISSN: 2319 – 4200, ISBN No: 2319 -4197), Nov-Dec.2012, pp.27-31.
4. **ICVCI-2011: International Conference on VLSI , Communication and Instrumentation**, April 7<sup>th</sup> to 9<sup>th</sup> 2011,SGIET, Kottayam, Kerala, Organized by Dept. of ECE and Applied Electronics & Instrumentation in association with International society of Automation(ISA) & IETE, Kerala.
5. **NCVSComs'12: National Conference on VLSI, Signal Processing and Communications**, Feb 10<sup>th</sup> to 11<sup>th</sup> 2012, Vignan University, Guntur , Organized by Dept. of ECE, Vignan University in association with IETE Chapter, Vijayawada.
6. **NCACT'12: National Conference on Advances in Communication & Technologies**, Jan,9<sup>th</sup> to 10<sup>th</sup> 2012, Gitam University, Visakhapatnam, Organized by Dept. of ECE, Gitam Institute of Technology, Gitam University & Sponsored by DST & MoES, Govt.of India,New Delhi.
7. **Waves'2008: A National Conference**, Feb 5<sup>th</sup>,2008, Organized by ECE Dept. Sri Kottam Tulasi Reddy Memorial College of Engineering, Kondair, Mahabub Nagar Dist, A P.
8. **Elysium'12: A National Level Technical & Cultural Fest**, March 15<sup>th</sup> to 17<sup>th</sup>, 2012, Holy Mary Institute of Technology & Science, Bogaram, Keesara Mdl, R R Dist, A P.

9. SSRG International Journal of VLSI & Signal Processing (SSRG-IJVSP), Vol. No: 2, Issue No: 6, (ISSN: 2394 – 2584), Nov-Dec, 2015, pp: 01-06.

### **Subjects Taught**

1. VLSI Design and VLSI Systems Design.
2. Digital Communications
3. Probability Theory and Stochastic Processes(PTSP)
4. Data Communications & Communication Theory.
5. STLD & DICA
6. EDC & ECA
7. Detection & Estimation of Signals (M.Tech)
8. Coding Theory & Techniques (M.Tech)
9. VLSI Technology & Design(M.Tech)
10. EMTL & Microwave Engineering

### **Additional Responsibilities Handled**

#### **NBA Accreditation**

- Member of ECE Department to carry out the NBA Accreditation work in Kottam Tulasi Reddy Memorial College of Eengineering, Kondair, Mahabub Nagar in 2009.

#### **NAAC Accreditation**

- NAAC Steering committee Co-ordinator of the college for the Holy Mary Institute of Technology & Science, Bogaram, Hyderabad in 2014.

### **Accomplishments**

- **Achieved 100% pass result in Optical Communications** for IV B.Tech ECE Students for the academic year 2010-2011.
- **Achieved 88.84% pass result in PTSP** for II B.Tech ECE Students for the academic year 2014-2015.
- **Achieved 93.75% pass result in EMTL** for the academic years 2009-2010.

### **Strengths**

- Self-motivated to pursue and disseminate knowledge, incorporate real-world experiences into coursework.
- Dedicated to the principles of academic integrity and intellectual honesty.

- Skilled at capturing students' attention and motivating them to learn.

### **Online Examinations**

- Member of Online Examinations since their inception from 2008.

### **Educational Tours**

- In-charge for Industrial Tours & Visits for ECE students from 2007 – 2009 in SKTRMCE, Kondair, Mahabub Nagar (Dist).
- Organized visits of III B.Tech students to SHAR Sriharikota, Nellore in Feb, 2015. .

### **Technical Forum**

- In-charge for Technical Forum where the activities include organizing Guest Lectures from various eminent persons in the respective field and encouraging students to give seminars on latest concepts.

### **College Day Committee**

- Convenor of Refreshments Committee for College Day Celebrations.
- Member of Cultural Committee for College Day Celebrations.

### **Counsellor**

- Counsellor for 20 Students in a Section and responsible for monitoring their marks and attendance and interacting with them once in a week and counselling them.

### **Workshops Attended**

- A 3-Days workshop in 'Teachers Training Program on Instructional Techniques' at SKTRMCE, Kondair from 25/07/2008 to 27/07/2008.
- A 2-Days workshop on 'VLSI Design' at SKTRMCE, Kondair from 13/02/2009 to 14/02/2009.
- A 5-Days workshop on 'LABVIEW' at INFOSYS Campus, Mysore 29/06/2009 to 03/07/2009.
- A 2-Weeks workshop on 'Signal Processing' at NIT, Warangal from 26/05/2008 to 07/06/2008.
- A 2-Weeks workshop on 'VLSI Design using VHDL' at SGGS, Nanded from 24/05/2004 to 05/06/2004.

- A 4-days workshop on “VLSI Design Methodology using Mentor Graphics” at G. Narayanamma Institute of Technology & Science for women, Hyderabad during 20<sup>th</sup> June, 2012 to 23<sup>rd</sup> June, 2012.

### **Workshops & Student Symposiums Organized**

- Member of the scrutinizing committee for students’ papers of WAVES – 2008, a National Level Paper Presentation for Students and Faculty Kondair, Mahabub Nagar.
- Organizing committee member of a two day short term course on ‘VLSI Design’ at SKTRMCE, Kondair, Mahabub Nagar.
- **Resource person** and organizing committee member of a two day short term course on ‘VLSI Design’ at IETE, OU Campus, Hyderabad.
- Organizing committee member of a two day short term course on ‘hands-on-experience’ by Sky-Fi Labs, Bangalore at ASIT, Gudur, Nellore.

### **Personal Profile:**

|                  |                            |
|------------------|----------------------------|
| Name:            | R Prabhakar                |
| Date of Birth:   | 01-08-1971                 |
| Father’s name:   | R Venkanna                 |
| Sex:             | Male                       |
| Marital Status:  | Married                    |
| Languages Known: | English, Hindi, and Telugu |
| Nationality:     | Indian                     |

### **Declaration:**

I hereby declare that the information furnished above is true to the best of my knowledge. I consider myself familiar with Electronics & Communications Engineering Aspects. I am also confident of my ability to work with team.

**Date:**

**Place: Hyderabad**

**(R PRABHAKAR)**